

Electronic Filing System (EFS) Data
Electronic Patent Application Submission
USPTO Use Only

EFS ID: 56949

Application ID: 10708563



Title of Invention:

STRUCTURE AND METHOD OF
FORMING A BIPOLAR
TRANSISTOR HAVING A VOID
BETWEEN EMITTER AND
EXTRINSIC BASE

First Named Inventor: Rama Divakaruni

Domestic/Foreign Application: Domestic Application

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